



The 4th Symposium on
ADVANCED TOPICS IN ELECTRICAL ENGINEERING

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Computer Aided Generation of DC & AC Electric Circuit Tests

Paul Dan Cristea, Rodica Ileana Tudea, Aurora Rodica Tudea

"Politehnica" University of Bucharest
Spl. Independentei 313, 77206 Bucharest, Romania,
Phone: +40 - 21- 411 44 37, Fax: +40 - 21- 410 44 14
e-mail:pcristea@dsp.pub.ro

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PAPER OUTLINE

1. Problem set description
2. Tree generation
3. Cotree generation
4. Tree plot
5. Graph plot
6. Circuit parameters and variables generation
7. Converting voltage sources to current sources
8. Introducing controlled sources
9. Web accessibility
10. Conclusions

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OBJECTIVES

Design and develop a software able to automatically generate large sets of circuit analysis problems, all with the same general features, but having different topological structures and parameters of the circuits.

Conditions:

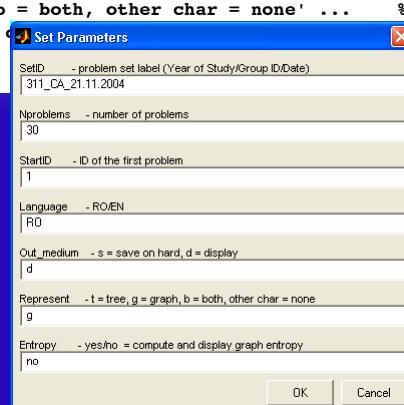
- The problems are for use both during the tutorials and for examinations, thus -- despite the inherent risk for an engineering perception of reality -- all parameters and variables describing the circuits should be integers to facilitate the computational task.
- Problems and solutions should be stored automatically on disk in distinct directories.
- Files referring to the same problem (text, graphics, etc) will have related labels.
- The system will be developed for making it accessible on the web.

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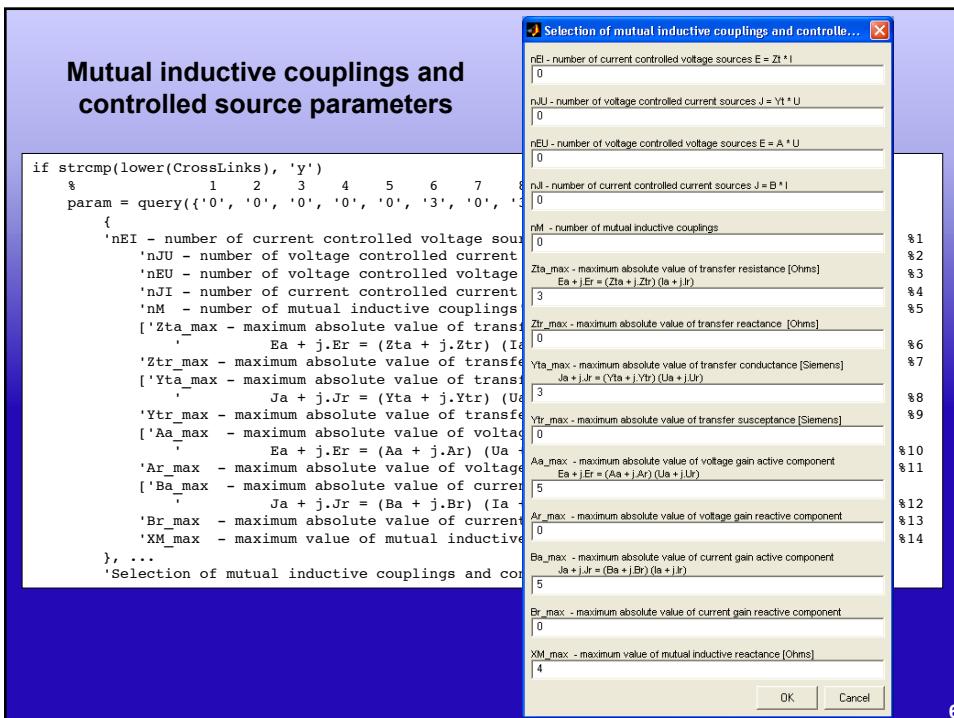
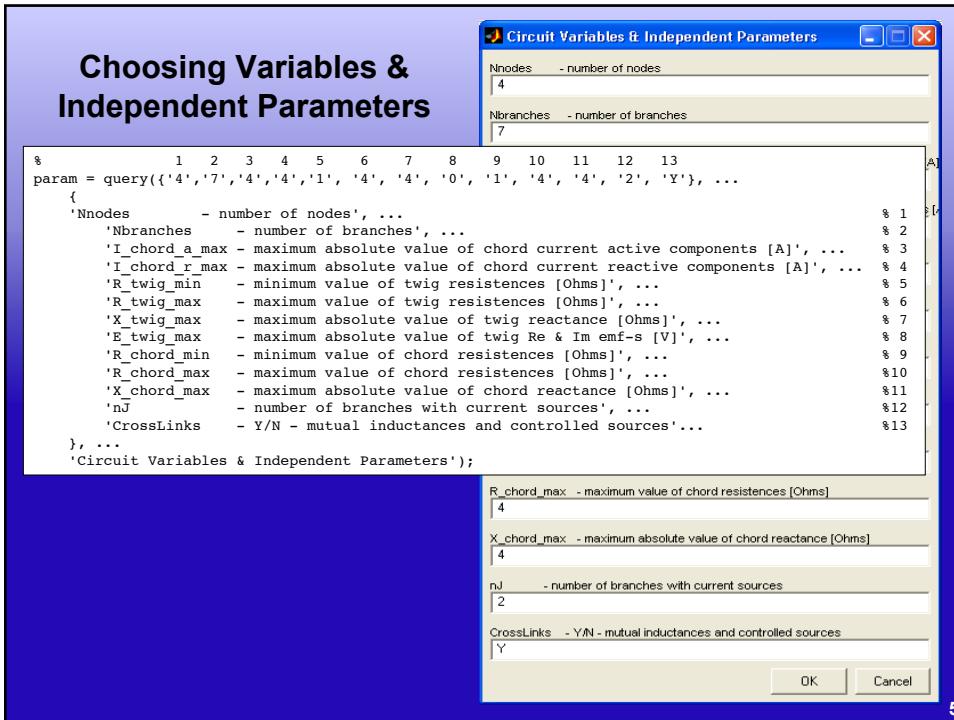
PROBLEM SET DESCRIPTION

Choosing the parameters of the set of AC problems to generate.

```
%          1   2   3   4   5   6   7
param = query({'311_CA_21.11.2004', '30','1','RO', 'd', 'g', 'no'}, ...
{
    'SetID'      - problem set label (Year of Study/Group ID/Date)', ... % 1
    'Nproblems'  - number of problems', ... % 2
    'StartID'    - ID of the first problem', ... % 3
    'Language'   - RO/EN', ... % 4
    'Out_medium' - s = save on hard, d = display' ... % 5
    'Represent'  - t = tree, g = graph, b = both, other char = none' ... % 6
    'Entropy'    - yes/no = compute and display graph entropy' ... % 7
}, ...
'Set Parameters');
```



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CIRCUIT TOPOLOGY

```
C_nodes_twigs = GenerateTree(Ntwigs, mode)
```

```
ShowTree(C_nodes_twigs, SetID, k)
```

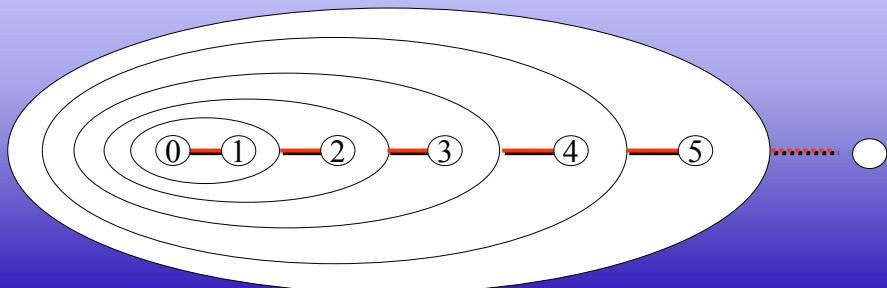
```
C_nodes_chords = GenerateCoTree(C_nodes_twigs, Nchords)
```

```
ShowGraphNet(C_nodes_twigs, C_nodes_chords, SetID, k)
```

```
C_twigs_chords = EssIncid(C_nodes_twigs, C_nodes_chords)
```

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Tree Generation



```
function C_nodes_twigs = GenerateTree(n, mode)
C_nodes_twigs = zeros(n, n);
rand('state',sum(100*clock));
r = rand(2,n);
c = 2 * ( r(2, :) >= 0.5 ) - 1;
m = 0;
for k = 1:n
    s = ceil((k-m)* r(1, k) + m-1 );
    f = k;
    if s>0, C_nodes_twigs(s, k) = c(k); end
    C_nodes_twigs(f, k) = - c(k);
    if mode == 's', m = s; end
end
```

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Examples

C_nodes_twigs =

```
-1 0 0 0 0 0 0 0 0
0 1 0 0 1 0 0 0 0
0 0 -1 -1 0 0 0 0 0
0 0 0 1 0 -1 0 0 0
0 0 0 0 -1 0 1 0 0
0 0 0 0 0 1 0 0 0
0 0 0 0 0 1 0 0 0
0 0 0 0 0 -1 1 0 0
0 0 0 0 0 0 0 -1 1
```

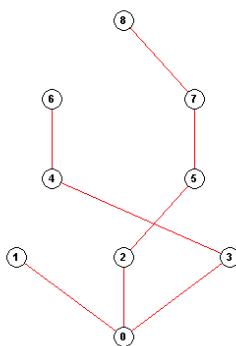
C_nodes_twigs =

```
1 -1 0 0 0 0 -1 0
0 1 0 0 -1 0 0 0 0
0 0 1 0 0 -1 0 0 0
0 0 0 1 0 0 0 0 0
0 0 0 0 1 0 0 0 0
0 0 0 0 0 1 0 0 -1
0 0 0 0 0 0 1 0 0
0 0 0 0 0 0 0 1 0
0 0 0 0 0 0 0 0 1
```

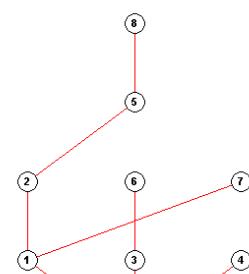
C_nodes_twigs =

```
-1 0 1 0 1 0 0 0 0
0 -1 0 0 0 0 0 1 0
0 0 -1 1 0 0 0 0 0
0 0 0 -1 0 0 0 0 0
0 0 0 0 -1 1 0 0 0
0 0 0 0 0 0 -1 1 0
0 0 0 0 0 0 0 -1 0
0 0 0 0 0 0 0 0 -1
0 0 0 0 0 0 0 0 0 -1
```

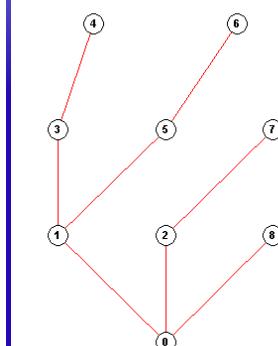
Tree8



Tree8



Tree8



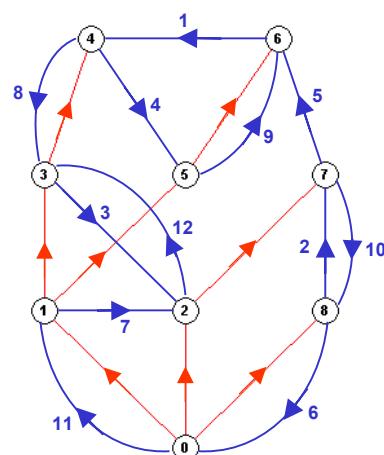
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Cotree Generation

Starts from the chosen tree

Chords are introduced between nodes chosen randomly from the class of nodes with the lowest rank (lowest number of connected branches).

This order assures the best connectivity of the circuit for a given number of chords.



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Code

```

function C_nodes_chords = GenerateCoTree(C_nodes_twigs, Nchords)

[n,m] = size(C_nodes_twigs);

if Nchords <= 0, C_nodes_chords = [ ]; return, end

C_nodes_chords_ext = zeros(n+1, Nchords);

rand('state', sum(100*clock)); r = rand(2,Nchords); c = 2 * ( r(2, :) >= 0.5 ) - 1;

C_nodes_twigs_ext = CompleteCnb(C_nodes_twigs);

Rank_All_Nodes = sum(abs((C_nodes_twigs_ext')));

Chord = 1;

while Chord <= Nchords

[Sorted_Rank_All_Nodes, Node_Line] = sort(Rank_All_Nodes);
Start_Node = Node_Line(1);

End_Node = Node_Line(2);

C_nodes_chords_ext(Start_Node, Chord) = c(Chord);
Rank_All_Nodes(Start_Node) = Rank_All_Nodes(Start_Node) + 1;

C_nodes_chords_ext(End_Node, Chord) = - c(Chord);
Rank_All_Nodes(End_Node) = Rank_All_Nodes(End_Node) + 1;

Chord = Chord + 1;
end
C_nodes_chords = C_nodes_chords_ext(2:(n+1), :);

```

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Examples

C_nodes_twigs =

```

-1 0 1 0 1 0 0 0 0
0 -1 0 0 0 0 1 0
0 0 -1 1 0 0 0 0 0
0 0 0 -1 0 0 0 0 0
0 0 0 0 -1 1 0 0 0
0 0 0 0 0 -1 1 0 0
0 0 0 0 0 0 -1 0 0
0 0 0 0 0 0 0 -1 0
0 0 0 0 0 0 0 0 -1

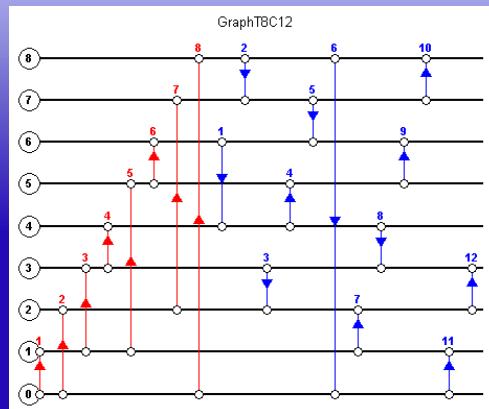
```

C_nodes_chords =

```

0 0 0 0 0 0 1 0 0 0 -1 0
0 0 -1 0 0 0 -1 0 0 0 0 1
0 0 1 0 0 0 0 -1 0 0 0 -1
-1 0 0 1 0 0 0 1 0 0 0 0
0 0 0 -1 0 0 0 0 1 0 0 0
1 0 0 0 -1 0 0 0 -1 0 0 0
0 -1 0 0 1 0 0 0 0 1 0 0
0 1 0 0 0 1 0 0 0 -1 0 0

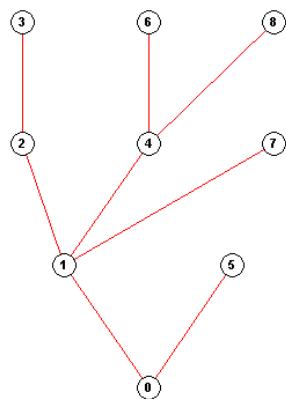
```



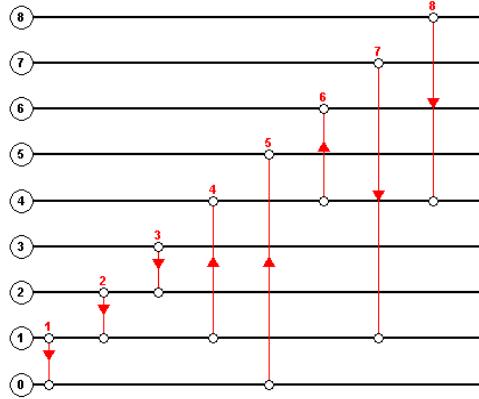
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Tree Plot

mode 0 Tree8



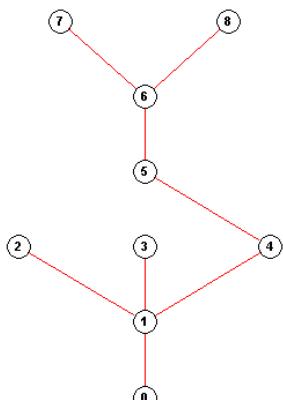
mode 0 GraphTBC1



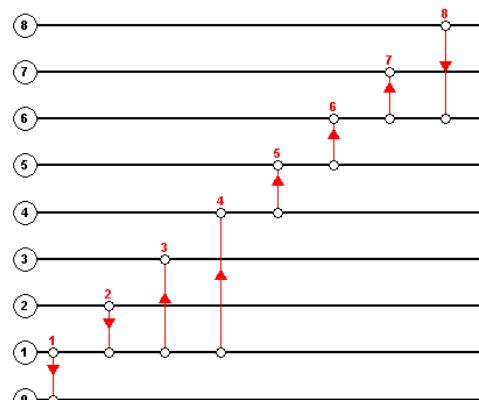
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's' Mode

mode s Tree8



mode s GraphTBC1



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Code 1

```
function ShowTree(C_nodes_twigs, SetID, k);
% Plots the tree corresponding to the C_nodes_twigs nodes-twigs incidence matrix
[n,m] = size(C_nodes_twigs);

FigName = 'Tree'; FigType = 'png';
f = sprintf("%s.%s", FigName, FigType);

y = ones(1, n+1);
y(0+1) = 0;
numY = zeros(1, n+1);
numX = zeros(1, n+1);

UAC = [abs(sum(C_nodes_twigs)); abs(C_nodes_twigs) - eye(n)];
[ii, jj] = find(UAC); % ii(i)-1 - index of node from where starts twig i, ending in node jj(i) = i
% the direction might be inverted

for i = 1:n
    y(i+1) = y(ii(i)) + 1; % y(i+1) ordinate of node i
End

ymax = max(y);

for i = 0:ymax
    numY(i+1) = sum(y == i);
    d = 1/(numY(i+1) + 1);
    kk = find(y == i);
    for j = 1:numY(i+1), x(kk(j)) = d * j; end
end

hFig = figure;
set(hFig, 'tag', strrep(FigName, '_', '_'));
axis([0, 1, 0, ymax+0.25]);
hold on
```

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Code 2

```
for i = 1:n
    plot([x(ii(i)), x(i+1)], [y(ii(i)), y(i+1)], '-r');
end

for i = 0:n
    plot(x(i+1), y(i+1), 'ok',...
        'MarkerEdgeColor', [0,0,0], ...
        'MarkerFaceColor', [1,1,1],...
        'MarkerSize',15);

    text('string', num2str(i),'position', [x(i+1), y(i+1)], 'FontSize', 8, ...
        'FontWeight','bold', 'Color', [0, 0, 0], ...
        'HorizontalAlignment', 'Center', 'VerticalAlignment', 'Middle');
end

ha = findobj(hFig, 'type', 'axes');
set(ha, 'Visible', 'off');
ht = title(strrep(FigName, '_', '_'));
set(ht, 'Color', [0 0 0], 'FontAngle', 'normal', 'FontName', 'Helvetica', ...
    'FontSize', [10], 'FontUnits', 'points', 'FontWeight', 'normal', ...
    'HorizontalAlignment', 'center', 'VerticalAlignment', 'bottom', 'Visible', 'on');

set(hFig, 'name', f);
f = makeUnique(f);
print( hFig, ['-d' FigType], f);
if ~isempty(k), close(hFig); end

return
```

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Graph Plot

```
function ShowGraphNet (C_nodes_twigs, C_nodes_chords, ...
SetID, k, ChordsNumbers)
```

C_nodes_twigs - the tree branches (twigs) to nodes incidence matrix

C_nodes_chords - the co-tree branches (chords) to nodes incidence matrix

SetID - label (tag) to identify the set of problems

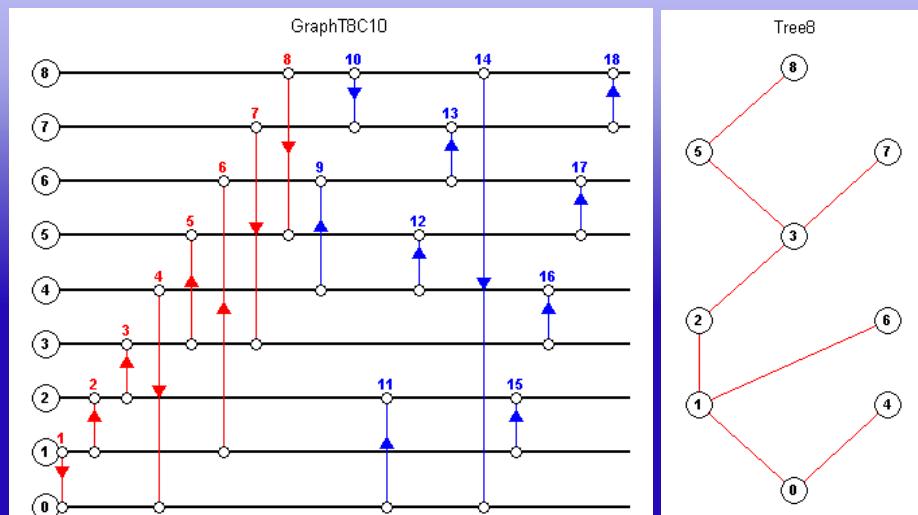
k - numerical identifier of the problem

ChordsNumbers = 0 - chords numbered independently,
from 1 to Nchords

ChordsNumbers = 1 - chords numbered in sequence
with twigs, from Ntwigs+1 to Ntwigs + Nchords

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Examples



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CIRCUIT PARAMETERS & VARIABLES

Circuit parameter and variable generation

```
[I, U, Z, E] = CircParam_AC(C_twigs_chords, ...
    I_chord_a_max, I_chord_r_max, ...
    R_twig_min, R_twig_max, X_twig_max, E_twig_max, ...
    R_chord_min, R_chord_max, X_chord_max);
```

Converting (some) voltage sources to current sources

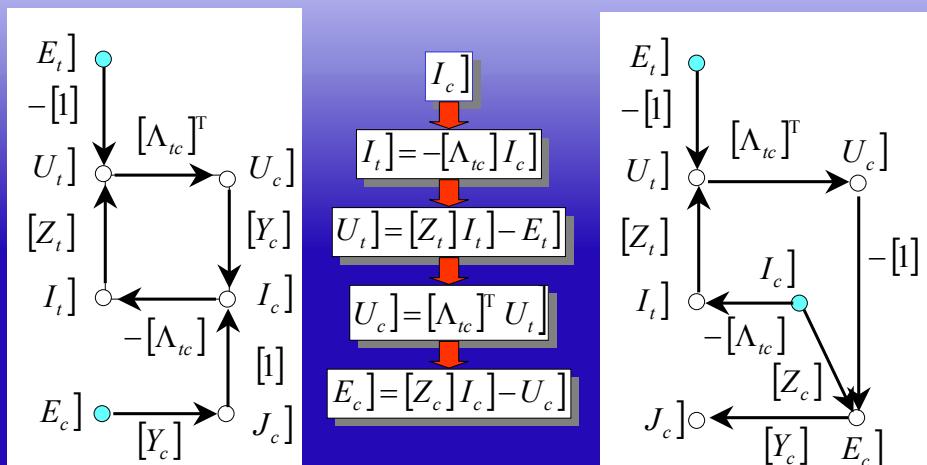
```
[E, J] = ConvertE2J_AC(E, Z, nJ);
```

Introducing controlled sources

```
[E, J, Zt, Yt, A, B, XM] = ControlledSources_AC(E, J, I, U, Z, ...
    nControl, nEI, nJU, nEU, nJI, nM, ...
    Zta_max, Ztr_max, Yta_max, Ytr_max, Aa_max, Ar_max, ...
    Ba_max, Br_max, XM_max);
```

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Circuit Parameter and Variable Generation



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Global Circuit Variables

Concatenate the matrices for tree & cotree

$$U] = \begin{bmatrix} U_t \\ U_c \end{bmatrix}$$

$$I] = \begin{bmatrix} I_t \\ I_c \end{bmatrix}$$

$$E] = \begin{bmatrix} E_t \\ E_c \end{bmatrix}$$

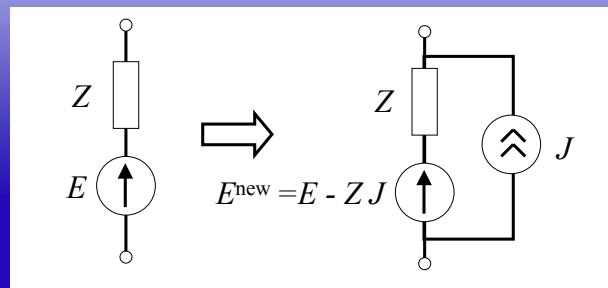
$$J] = \begin{bmatrix} J_t \\ J_c \end{bmatrix}$$

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Converting Voltage Sources to Current Sources

Current sources (change of independent voltage source emf's)

$$E^{\text{new}}] = E] - [Z] J]$$



Convert nJ voltage sources to current sources

$$[E, J] = \text{ConvertE2J_AC}(E, Z, nJ);$$

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Cross Parameters

```
[E, J, Zt, Yt, A, B, XM] = ControlledSources_AC(E, J, I, U, Z, ...
nControl, nEI, nJU, nEU, nJI, nM, ...
Zta_max, Ztr_max, Yta_max, Ytr_max, ...
Aa_max, Ar_max, Ba_max, Br_max, XM_max);
```

Controlled sources (change of independent voltage source emf's)

$$\begin{array}{ll} E^{controlled} = [Z_t] I & E^{\text{new}} = E - [Z_t] I \\ J^{controlled} = [Y_t] U & E^{\text{new}} = E - [Z] [Y_t] U \\ E^{controlled} = [A] U & E^{\text{new}} = E - [A] U \\ J^{controlled} = [B] I & E^{\text{new}} = E - [Z] [B] I \end{array}$$

Mutual reactances

$$E^{induced} = [-j X_M] I \quad E^{\text{new}} = E - E^{\text{induced}}$$

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WEB ACCESSIBILITY

The system will be accessible on the INTERNET, to allow remote use, for both professors and students

Partial examination of problems will be done on the computer, In a face-to-face or remote setting.

The web accessibility is currently partially functional and partially under development

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```

function retstr = webCAM(instruct, outfile)
% webCAM returns circuit parameters into HTML table.
% webCAM(INSTRUCT) returns output in RETSTR.
% webCAM(INSTRUCT, OUTFILE) returns output in RETSTR.
% OUTFILE is a valid spec for test output.
%
% INSTRUCT is a structure created by the matweb program.
% It contains fields corresponding to the HTML form fields
% in the HTML form, webCAM1.html. In webCAM1.html there
% is a hidden field, called INSTRUCT.MLMFILE that references
% this webCAM.m M-file.

function rs = webCircuitPlot(h)
% RS = webCircuitPlot(H) creates a plot of graph with handle h and
% returns HTML output in string RS. Handle h is the
% structure created by matweb. It contains variables
% from the HTML input form in Circuit_plot_generator.html

```

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CONCLUSIONS

- A specialized e-learning system able to automatically generate large sets of circuit analysis problems, all with the same difficulty, but having different topological structures and parameters of the Circuits, has been designed, implemented and experimented.
- The problems are for use both during the tutorials and for examinations, thus -- despite the inherent risk for an engineer understanding of reality -- all parameters and variables describing the circuits should be integers to facilitate the computational task.
- Problems and solutions should be stored automatically on disk in distinct directories, with files referring to the same problem having related labels
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